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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10087566	FILING DATE 03/01/2002	CLASS 428	SUBCLASS 469	GAU 1775	EXAMINER Xu
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**APPLICANTS: Furo Masahiro; Osanai Hideyo;

**CONTINUING DATA VERIFIED:

None CX

** FOREIGN APPLICATIONS VERIFIED:

JAPAN 056106/2001 03/01/2001

JAPAN 016579/2002 01/25/2002

CX

PG-PUB DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO 134.142
Verified and Acknowledged Examiner's initials <i>luy Xu</i>		
TITLE : Insulating substrate boards for semiconductor and power modules		
U.S.DEP'T. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
		DRAWING	
Assistant Examiner		Sheets Drwg. Figs. Drwg. Print Fig.	
Primary Examiner			
PREPARED FOR ISSUE		Application Examiner	
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